

**AMENDMENTS TO THE SPECIFICATION:**

Please replace the paragraph beginning on page 8, line 24 with the following amended paragraph:

"In some aspects, the switch 208 is a transistor with a terminal connected to capacitor 202 on line 204, a terminal connected to ground 206 on line 210, and a control terminal connected to the logic unit 130 output on line 212. The transistor is enabled in response to accepting the test control signal, creating a signal path between lines 204 and 210. In some aspects, the transistor is a field effect transistor (FET) or a bi-polar junction transistor (BJT). In FIG. 1, an FET 214 is shown. The diode-~~216~~-218 in the FET 214 is oriented so that the FET 214 conducts to ground only when the FET 214 is enabled. "